

Course code	Course Name	L-T-P - Credits	Year of Introduction
EC204	ANALOG INTEGRATED CIRCUITS	4-0-0-4	2016
Prerequisite: Nil			
Course Objectives			
<ul style="list-style-type: none"> • To equip the students with a sound understanding of fundamental concepts of operational amplifiers • To understand the wide range of applications of operational amplifiers • To introduce special function integrated circuits To introduce the basic concepts and types of data converters 			
Syllabus			
Differential amplifier configurations, Operational amplifiers, Block diagram, Ideal op-amp parameters, Effect of finite open loop gain, bandwidth and slew rate on circuit performance, op-amp applications-linear and nonlinear, Active filters, Specialized ICs and their applications, Monolithic Voltage Regulators - types and its applications, Data converters - specifications and types.			
Expected outcome .			
The students will			
<ol style="list-style-type: none"> i. have a thorough understanding of operational amplifiers ii. be able to design circuits using operational amplifiers for various applications 			
Text Book:			
<ol style="list-style-type: none"> 1. Franco S., Design with Operational Amplifiers and Analog Integrated Circuits, 3/e, Tata McGraw Hill, 2008 2. Salivahanan S. ,V. S. K. Bhaaskaran, Linear Integrated Circuits, Tata McGraw Hill, 2008 			
References:			
<ol style="list-style-type: none"> 1. Botkar K. R., Integrated Circuits, 10/e, Khanna Publishers, 2010 2. C.G. Clayton, Operational Amplifiers, Butterworth & Company Publ. Ltd. Elsevier, 1971 3. David A. Bell, Operational Amplifiers & Linear ICs, Oxford University Press, 2nd edition, 2010 4. Gayakwad R. A., Op-Amps and Linear Integrated Circuits, Prentice Hall, 4/e, 2010 5. R.F. Coughlin & Fredrick Driscoll, Operational Amplifiers & Linear Integrated Circuits, 6th Edition, PHI,2001 6. Roy D. C. and S. B. Jain, Linear Integrated Circuits, New Age International, 3/e, 2010 7. Sedra A. S. and K. C. Smith, Microelectronic Circuits, 6/e, Oxford University Press, 2013 			
Course Plan			
Module	Contents	Hours	Sem. Exam Marks
I	Differential amplifiers: Differential amplifier configurations using BJT, Large and small signal operations, Input resistance, Voltage gain, CMRR, Non-ideal characteristics of differential amplifier. Frequency response of differential amplifiers, Current sources, Active load, Concept of current mirror circuits, Wilson current mirror circuits (Analysis using hybrid 'pi' model only).	6	15%
	Operational amplifiers: Introduction, Block diagram, Ideal op-amp parameters, Equivalent circuit, Voltage transfer curve, Open loop op-amp configurations, Effect of finite open loop gain, Bandwidth and slew rate on circuit performance	5	
II	Op-amp with negative feedback: Introduction, Feedback	3	15%

	configurations, Voltage series feedback, Voltage shunt feedback, Properties of practical op-amp.		
	Op-amp applications: Inverting and non inverting amplifier, DC and AC amplifiers, Summing, Scaling and averaging amplifiers, Instrumentation amplifier.	4	
FIRST INTERNAL EXAMINATION			
III	Op-amp applications: Voltage to current converter, Current to voltage converter, Integrator, Differentiator, Precision rectifiers, Log and antilog amplifier, Phase shift and Wien bridge oscillators	7	15%
IV	Astable and monostable multivibrators, Triangular and saw tooth wave generators, Comparators, Zero crossing detector, Schmitt trigger	5	15%
	Active filters: Advantages, First and second order low pass, High pass, Band pass and band reject filters, Design of filters using Butterworth approximations	5	
SECOND INTERNAL EXAMINATION			
V	Specialized ICs and its applications: Timer IC 555 : Astable and monostable operations, applications. Analog Multipliers: Introduction, Gilbert multiplier cell. Voltage Controlled Oscillator IC AD633 and their applications.	3	20%
	Phase Locked Loop – Operation, Closed loop analysis, Lock and capture range, Basic building blocks, PLL IC 565, Applications of PLL for AM & FM detection and Frequency multiplication, Frequency division, Frequency synthesizing.	4	
	Monolithic Voltage Regulators - Fixed voltage regulators, 78XX and 79XX series, Adjustable voltage regulators, IC 723 – Low voltage and high voltage configurations, Current boosting, Current limiting, Short circuit and Fold-back protection.	4	
VI	Data Converters: D/A converter, Specifications, Weighted resistor type, R-2R Ladder type.	3	20%
	A/D Converters: Specifications, Classification, Flash type, Counter ramp type, Successive approximation type, Single slope type, Dual slope type, Sample-and-hold circuits.	5	
END SEMESTER EXAM			

Assignment

1. Explain the importance of frequency compensated networks in opamps and the commonly used compensation techniques.
2. Write short notes on commercially available integrated circuits (Opamp, ADC, DAC, VCO, Analog multiplier, PLL) with pin outs and their important features

Question Paper Pattern

The question paper shall consist of three parts. Part A covers I and II module, Part B covers III and IV module, Part C covers V and VI module. Each part has three questions, which may have maximum four subdivisions. Among the three questions, one will be a compulsory question covering both modules and the remaining from each module, of which, one to be answered. Part A & Part B questions shall carry 15 marks each and Part C questions shall carry 20 marks each with maximum 60% for theory and 40% for logical/numerical problems, derivation and proof.